

GOVT. ENGINEERING COLLEGE AJMER

Department of Electronics & Communication Engineering

Ref. No. 1026


I Mid Term Test, 2017-18

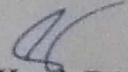
Date: 08/02/2018

Date	B. Tech IV Sem.	SESSION
14.02.2018	4EC1A ANALOG ELECTRONICS	S-I
14.02.2018	4EC2A RANDOM VARIABLES & STOCHASTIC. PROC.	S-IV
15.02.2018	4EC3A ELECTRONIC MEASUREMENT & INST.	S-I
15.02.2018	4EC4A ELECTROMAGNETIC FIELD THEORY	S-IV
16.02.2018	4EC5A OPTIMIZATION TECHNIQUES	S-I
16.02.2018	4EC6A ADVANCED ENGINEERING MATHEMATICS II	S-IV
	<i>B. Tech VI Sem.</i>	
14.02.2018	6EC1A MICROWAVE ENGINEERING-II	S-II
14.02.2018	6EC2A MICROPROCESSORS	S-V
15.02.2018	6EC3A INDUSTRIAL ELECTRONICS	S-II
15.02.2018	6EC4A DIGITAL COMMUNICATION	S-V
16.02.2018	6EC5A CONTROL SYSTEMS	S-II
16.02.2018	6EC6.3A OPTICAL FIBER COMMUNICATION	S-V
	<i>B. Tech VIII Sem.</i>	
14.02.2018	8EC1A- IC TECHNOLOGY	S-III
14.02.2018	8EC2A RADAR & TV ENGINEERING	S-VI
15.02.2018	8EC3A MEMS & NANOTECHNOLOGY	S-III
15.02.2018	8EC4.1A COMPUTER NETWORKS	S-VI

Sitting Plan: R. No. 1 to 60 in NB-112, Rest all Incl. Back Students in NB-113

Time Schedule: S-I: 8:15 - 9:15 AM S-II: 9:30 - 10:30 AM
S-III: 10:45 - 11:45 AM S-IV: 12:00 - 1:00 PM
S-V: 1:15 - 2:15 PM S-VI: 2:30 - 3:30 PM


(Dept. Exam Coordinator)


(Head, ECE)

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 - Concerned faculty members by circulation,
- Main Coordinator (Exams.)
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