

**GOVERNMENT ENGINEERING COLLEGE AJMER**  
**DEPARTMENT OF COMPUTER SCIENCE**  
**DUTY CHART OF PRACTICAL EXAMINATION FOR B. TECH. V SEM & VII SEM (CS & IT)**

15/10/18

REF. NO.: ECA/CS & IT/2018/ 164

DATE/ DAY	BRANCH	SEM	GROUP	TIME	PRACTICAL	LAB
20/10/2018 SAT	CS	VII	C1	9:00 AM-12:00 AM	VLSI Physical Design Lab	G-13
			C2	12:00 AM-3:00 PM		
			C3	9:00 AM-12:00 AM	Compiler Design Lab	G-18 A
			C4	12:00 AM-3:00 PM		
	IT	VII	I1	9:00 AM-12:00 AM	Practical Training*	A
			I2	12:00 AM-3:00 PM		
			I1	12:00 AM-3:00 PM	Discipline & Extra Curricular Activities	B
			I2	9:00 AM-12:00 AM		
	CS	V	C1	9:00 AM-12:00 AM	System Design in UML Lab.	C
			C2	12:00 AM-3:00 PM		
			C3	9:00 AM-12:00 AM	Discipline & Extra Curricular Activities	D
			C4	12:00 AM-3:00 PM		
IT	V	I1	9:00 AM-12:00 AM	Advance Communication Lab.	A-COM	
		I2	12:00 AM-3:00 PM			
22/10/2018 MON	CS	VII	C1	9:00 AM-12:00 AM	Compiler Design Lab	G-18 A
			C2	12:00 AM-3:00 PM		
			C3	9:00 AM-12:00 AM	Web Development Lab	A
			C4	12:00 AM-3:00 PM		
	IT	VII	I1	9:00 AM-12:00 AM	DMW Lab	D
			I2	12:00 AM-3:00 PM		
	CS	V	C1	9:00 AM-12:00 AM	Digital Hardware Design Lab	G-13
			C2	12:00 AM-3:00 PM		
			C3	9:00 AM-12:00 AM	DataBase Lab	C
			C4	12:00 AM-3:00 PM		
	IT	V	I1	9:00 AM-12:00 AM	Operating Systems Simulation Lab	B
			I2	12:00 AM-3:00 PM		
23/10/2018 TUE	CS	V	C1	9:00 AM-12:00 AM	DataBase Lab	D
			C2	12:00 AM-3:00 PM		
			C3	9:00 AM-12:00 AM	Operating Systems Simulation Lab	B
			C4	12:00 AM-3:00 PM		
	IT	V	I1	9:00 AM-12:00 AM	Discipline & Extra Curricular Activities	A
			I2	12:00 AM-3:00 PM		
24/10/2018 WED	CS	VII	C1	9:00 AM-12:00 AM	Web Development Lab	A
			C2	12:00 AM-3:00 PM		
			C1	12:00 AM-3:00 PM	Discipline & Extra Curricular Activities	D
			C2	9:00 AM-12:00 AM		
	IT	VII	C3	9:00 AM-12:00 AM	VLSI Physical Design Lab	G-18 A
			C4	12:00 AM-3:00 PM		
			I1	9:00 AM-12:00 AM	Internet Programming Lab.	G-18 B
			I2	12:00 AM-3:00 PM		
	CS	V	C1	9:00 AM-12:00 AM	Discipline & Extra Curricular Activities	B
			C2	12:00 AM-3:00 PM		
			C3	9:00 AM-12:00 AM	Digital Hardware Design Lab	G-13
			C4	12:00 AM-3:00 PM		
IT	V	I1	9:00 AM-12:00 AM	DataBase Lab	C	
		I2	12:00 AM-3:00 PM			
25/10/2018 THU	CS	VII	C1	9:00 AM-12:00 AM	Practical Training	G-18 B
			C2	12:00 AM-3:00 PM		
			C3	12:00 AM-3:00 PM	Discipline & Extra Curricular Activities	G-18 A
			C4	9:00 AM-12:00 AM		
	IT	VII	C3	9:00 AM-12:00 AM	Practical Training*	C
			C4	12:00 AM-3:00 PM		
			I1	9:00 AM-12:00 AM	Computer Graphics & Multimedia Lab	A
			I2	12:00 AM-3:00 PM		
	CS	V	C1	9:00 AM-12:00 AM	Operating Systems Simulation Lab	B
			C2	12:00 AM-3:00 PM		
			C3	9:00 AM-12:00 AM	System Design in UML Lab.	D
			C4	12:00 AM-3:00 PM		
IT	V	I1	9:00 AM-12:00 AM	Digital Hardware Design Lab	G-13	
		I2	12:00 AM-3:00 PM			

*Dilip*  
*Deepak*  
*Shikha*  
 15/10/18  
 DILIP SISODIA/DEEPAK GUPTA/SHIKHA GUPTA  
 EXAM COORDINATORS (CS IT)

*Rathi*  
 DR. RAKESH RATHI  
 HEAD (CS IT)

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