

G1601/CS/20/2020/3073
15/01/2020

Sheet1

ENGINEERING COLLEGE RSMER

DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

SESSION 2019-20

TIME TABLE: 8th SEM COMPUTER ENGINEERING

WEF: 13/1/2020

DAY/ TIME	9:00 AM 10:00 AM	10:00 AM 11:00 AM	11:00 AM 12:00 PM	12:00 PM 1:00 PM	1:00 PM 2:00 PM	2:00 PM 3:00 PM	
MON	RTS (AC) S-12	MC (NS) S-12	DIP (SNT) S-12	C4- FPGA LAB A(DS)			
TUE	C3- SEMINAR LAB QEEE (SK) C4- DIP LAB G-18 A(SNT)			MC (NS) S-12	C2- PRO-II LAB C (SNT) C3- DIP LAB B(SG)		
WED	RTS (AC) S-12	DIP (SNT) S-12	DS (DS) S-12	C1- FPGA LAB G-18 B(DS)			
THU	MC (NS) S-12	DS (DS) S-12	RTS (AC) S-12	C1- DIP LAB G18-A (SNT) C2- SEMINAR LAB QEEE(RS) C4- PROJ LAB C(DS)			
FRI	C1- SEMINAR LAB QEEE(RS) C3- Proj LAB C(DS) C2- FPGA LAB G18-B(SC)			DS (DS) S-12	C1- UNP LAB C(PT) C4- UNP-LAB D(VJ)		
SAT	C1- PRO-II LAB G 18 A(SNT) C4- SEMINAR LAB QEEE(SK)			DIP (SNT) S-12	C3- FPGA-LAB CC(DS)		
	C2- UNP LAB C(PT) C3- UNP LAB D (VJ)			C2- DIP LAB A(SNT)			
MC: Dr. Neetu Sharma DIP: Mr. S N Tazi DS: Mr. Dillip Sisodia RTS: Mr. Anita Choudhary				UNPS LAB(C1,C2,C3,C4): Mr. Vinesh Jain+Dr. Prakriti Trivedi FPGA LAB(C2): Mrs. Suman Choudhary FPGA LAB(C1,C3,C4): Mr. Dillip Sisodia DIP LAB(C1,C2,C3,C4): Mr. S N Tazi +Mrs Shikha Gupta PRO-II LAB(C1,C2,C3,C4): Mr. S N Tazi+Mr. Dillip Sisodia SEMINAR LAB(C1,C2,C3,C4): Mr Ravinder Singh+ Mr. Sunil Khinchi		Vinesh Jain H R Choudhary Time Table Incharge(CSE) Dr. Rakesh RSKhi Head (CSE)	

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