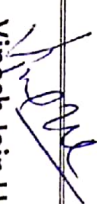
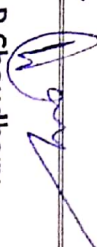


EC/A/CSE/2020/3384  
18/03/2020

DAY/ TIME	9:00 AM 9:50 AM	9:50 AM 10:40 AM	10:40 AM 11:30 PM	11:30 PM 12:20 PM	12:20 PM 1:10 PM	1:10 PM 2:00 PM	2:00 PM 2:50 PM
MON	RTS (AC) S-12	MC (NS) S-12	DIP (SNT) S-12	C4- FPGA LAB A(DS) C2- PRO-II LAB C (SNT) C3- DIP LAB B(SG)			RTS (AC) S-12
TUE	C1- FPGA LAB G-18 B(DS)			MC (NS) S-12	C1- PRO-II LAB G -13(SNT) C4- SEMINAR LAB QEEE(SK)		
WED	MC (NS) S-12	DIP (SNT) S-12	DS (DS) S-12	C1- DIP LAB G18-A (SNT) C4- PROJ LAB C(DS) C3- UNP LAB D (VJ) C2- UNP LAB C(PT) C1- UNP LAB C(PT) C4- UNP-LAB D(VJ)			
THU	DIP (SNT) S-12	DS (DS) S-12	RTS (AC) S-12	C2- DIP LAB A(SNT) C3- FPGA-LAB CC(DS) C2- SEMINAR LAB QEEE(RS)			
FRI	C2- FPGA LAB G18-B(SC)			DS (DS) S-12	C2- SEMINAR LAB QEEE(RS)		
SAT	<p>MC: Dr. Neetu Sharma DIP: Mr. S N Tazi DS: Mr. Dilip Sisodia RTS: Mr. Anita Choudhary</p> <p>UNPS LAB(C1, C2, C3, C4): Mr. Vinesh Jain+Dr. Prakriti Trivedi FPGA LAB(C2): Mrs. Suman Choudhary FPGA LAB(C1, C3, C4): Mr. Dilip Sisodia DIP LAB(C1, C2, C3, C4): Mr. S N Tazi +Mrs Shikha Gupta PRO-II LAB(C1, C2, C3, C4): Mr. S N Tazi+Mr. Dilip Sisodia SEMINAR LAB(C1, C2, C3, C4): Mr Ravinder Singh+ Mr. Sunil Khinchi</p> <p style="text-align: right;">                         Vinesh Jain H R Choudhary                      Time Table Incharge(CSE)                      Dr. Rakesh RSKhi                      Head (CSE)                 </p>						

Copy to:

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