

# ENGINEERING COLLEGE AJMER

DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

Practical Time Table VIII Sem CS and IT (Main and Back) 2019-20

*ECA/CSE/2020/3742*

*DT: 24/09/2020*

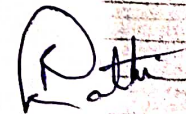
DATE / DAY	TIME	BRANCH	SUBJECT NAME	VENUE (If Offline)
01/10/2020 THURS	10:00 to 12:00	C1	UNP Lab	Lab B
		C3	FPGA	Lab C
		I1	ST Lab	Lab G-18
	12:30 to 2:30	C2	UNP Lab	Lab B
		C4	FPGA	Lab C
		I2	ST Lab	Lab G-18
03/10/2020 SAT	10:00 to 12:00	C1	FPGA	Lab B
		C3	UNP Lab	Lab C
		I1	AWP Lab	Lab G-18
	12:30 to 2:30	C2	FPGA	Lab B
		C4	UNP Lab	Lab C
		I2	AWP Lab	Lab G-18
05/10/2020 MON	10:00 to 12:00	C1	DIP Lab	Lab B
		C3	Seminar	Lab C
		I1	DIP Lab	Lab G-18
	12:30 to 2:30	C2	DIP Lab	Lab B
		C4	Seminar	Lab C
		I2	DIP Lab	Lab G-18
06/10/2020 TUES	10:00 to 12:00	C1	Seminar	Lab B
		C3	DIP Lab	Lab C
		I1	MAD Lab	Lab G-18
	12:30 to 2:30	C2	Seminar	Lab B
		C4	DIP Lab	Lab C
		I2	MAD Lab	Lab G-18
07/10/2020 WED	10:00 to 2:30	C1, C2, C3, C4	Project (Combined for all batches)	Computer Center
		I1, I2	Project (Combined for all batches)	Lab C
08-10-2020 THURS	10:00 to 12:00	I1	Seminar	Lab C
	12:30 to 2:30	I2	Seminar	Lab C

**Instructions:**

- 1.) Student can give the practical examination in either online or offline mode.
- 2.) External and Internal Examiners will collect the student concern for mode of practical examination and will arrange their examination accordingly i.e. either online, offline or both.
- 3.) Practical exam will be conducted based on the components like quiz, viva and online submission of report.
- 4.) Hard copy of the project report and seminar should be submitted by the student in person or through post (whatever suitable for student).
- 5.) List of Back students enrolled for practical back examination is available at college website.

  
Deepak Gupta

IV Year (CE & IT) Exam Coordinator

  
Dr. Rakesh Rathi  
Head (CSE)

Copy To (through website):

- 1 Principal for kind information
- 2 UG Coordinator
- 3 All Concern Faculties
- 4 College Website
- 5 Department File