

ENGINEERING COLLEGE AJMER
TIME TABLE-2022-23(TENTATIVE)

VII-SEM.(ECE)

Ref. No.: ECA/ECE/2022/14356

w.e.f. 10-09-2022

Period	1 09:00 – 10:00	2 10:00 – 11:00	3 11:00 – 12:00	12:00 – 13:00	4 13:00 – 14:00	5 14:00 – 15:00	6 15:00 – 16:00
MON		VLSI Design (HKS) (NB-113)	ITS(EC1) (RKR)	Opt. Comm. Lab(EC2)RKR(NB-226)			
				Seminar (EC1) USM			
TUE		EEDM (PJ) (NB-113)	ITS(EC2) (RKR)	Opt. Comm. Lab(EC1)RKR(NB-226)			
				Adv. Comm. Lab(EC2) DJ(NB-102)			
WED		VLSI Design (HKS) (NB-113)	ITS(EC3) (AG)	VLSI Design Lab (EC2)HKS(NB-108)			
				Adv. Comm. Lab(EC3) DK(NB-102)			
THU		EEDM (PJ) (NB-113)		VLSI Design Lab (EC3)HKS(NB-108)			
				Seminar (EC2) USM			
FRI		VLSI Design (HKS) (NB-113)		VLSI Design Lab (EC1)HKS(NB-108)			
				Seminar (EC3) USM			
SAT		EEDM (PJ) (NB-113)		Adv. Comm. Lab(EC1) DJ(NB-102)			
				Opt. Comm. Lab(EC3)RKR(NB-226)			

Subject : Program Elective – VLSI Design

Open Elective - Environmental Engineering and Disaster Management (EEDM)

Note: USM – Uma Shankar Modani
RKR-Rajesh Kumar Raj

AG – Anurag Garg
HKS- Harish Kumar Sharma

DJ – Deepak Jhanwer
PJ – Pooja Tomar


(Dept. Time Table Incharge)


(Head ECE)

Copy to (By college website)

- 1) Principal for kind information
- 2) Main Coordinator Academics

- 3) All Heads/ I/C Heads
- 4) Time Table Incharge

- 5) All Notice Boards
- 6) Guard File