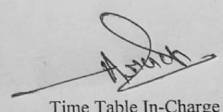


Government Engineering College, Ajmer
Department of Electrical Engineering
Btech, Vth Sem, Session-2019-20
Time Table

CIECA/EE/2019/AA-2
15/7/19

WEE - 15/07/19

Day	Time	8:00-8:50 AM	8:50-9:40 AM	9:40-10:30 AM	10:30-10:45 AM	10:45-11:35 AM	11:35-12:25 PM	12:25-1:15 PM	1:15-2:00 PM
Monday			CS Lab EE-1 C- Lab			EMD	SP Lab EE-1 C Lab		
			PS-1 Lab EE-2 PS Lab			S-17	MPC Lab EE-2 MPC Lab		
Tuesday	MPC S-17		PS-1 S-17	EM S-17		Training Seminar	RPS SD F-12		
Wednesday			MPC F-12	EMD F-12		MPC Lab EE-1 PMC Lab		CS F-12	
						CS Lab EE-2 C- Lab			
Thursday			PS-1 F-12	CS F-12		MCP F-12			
Friday			RPS F-12	EMD F-12		CS F-12	EM F-12		
Saturday		SP Lab EE-2 C Lab		PS-1 S-17		Training Seminar	EM F-12		
		PS-1EE-1 PS Lab							



Time Table In-Charge



HOD(EE)

Copy To:-

1. Principal for Kind Information
2. Main Co-ordinator Academics
3. Notice Board
4. Concern Faculty Members