



ENGINEERING COLLEGE- AJMER
(AN AUTONOMOUS INSTITUTION OF GOVT. OF RAJASTHAN)
NH-8, BARLIYA CIRCLE AJMER (RAJASTHAN)-305025
Department of Electrical Engineering

GECA/EE/2019/638

Session 2019-20
Practical Exam Time Table

Date- 15/11/2019

III rd Semester				
Subject	Batch	Date	Time	Venue
Electrical Circuit Design Lab	EE-1	18/11/2019	9:00- 11:00	Computer Lab
	EE-2	18/11/2019	12:00- 2:00	Computer Lab
Electrical Machine Lab	EE-1	19/11/2019	9:00- 11:00	Electrical Machine Lab
	EE-2	19/11/2019	12:00- 2:00	Electrical Machine Lab
Analog Electronics Lab	EE-1	20/11/2019	9:00-11:00	Analog Electronics Lab
	EE-2	20/11/2019	12:00- 2:00	Analog Electronics Lab
Industrial Training Lab	EE-1	21/11/2019	9:00- 11:00	Power System Lab
	EE-2	21/11/2019	12:00- 2:00	Power System Lab
Social Outreach, Discipline & Extra-Curricular Activities	EE-1	22/11/2019	9:00- 11:00	Computer Lab
	EE-2	22/11/2019	12:00- 2:00	Computer Lab


Exam Coordinator


HOD, EE Department

Copy To-

1. P.A. to Principal for kind Information
2. Main coordinator Academics
3. EE, Humanities Department HODs,
4. Time Table In-charge
5. Notice Board/Website