

**Govt. Engineering College, Ajmer**  
**Department of Electronics Instrumentation & Control Engineering**

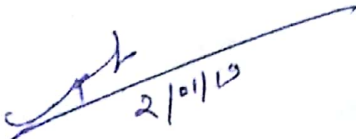
No. GECA/EI&CE/2019/ 301 - 303  
Date: 02/01/2019

**Practical Examination Time-Table Schedule**

Day & Date	III Semester		III Semester	
	Time	Subject	Time	Subject
07/01/2019 Monday	08:30 Onwards	Signal Processing Lab (F-17)		-
08/01/2019 Tuesday	8:30 -11:30 AM	Computer Lab (F-17)	11:30 - 01:30 PM	Industrial Training Seminar (F-21)
09/01/2019 Wednesday	8:30 -11:30 AM	Digital system design Lab (S-3A)		-
10/01/2019 Thursday	8:30 -11:30 AM	Electronics Devices Lab (s-16)	11:30 - 01:30 PM	Social Outreach, Discipline & Extra Curricular Activities (F-21)

Note:

Group wise Time schedule and sitting arrangement will be done by course coordinator/Internal Examiner within stipulated time schedule as given in above table.

  
2/01/19

Time Table In-charge

  
2/01/19  
Head EI&CE

Copy to,

1. Principal for Kind information.
2. Computer dept Head
3. Time Table In-charge.
4. Notice Board/website
5. All Faculty Members